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Term	Documents
HORIZONTAL	653049
HORIZONTALS	297
SCAN	126162
SCANS	60358
BLANKING	22392
BLANKINGS	55
PERIOD	882743
PERIODS	276008
WRITE	139983
WRITES	46190
DATA	805062
(L3 AND HORIZONTAL SCAN SAME BLANKING ADJ PERIOD SAME WRITE ADJ DATA).USPT	2

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Database:

- US Patents Full Text Database
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- JPO Abstracts Database
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- Derwent World Patents Index
- IBM Technical Disclosure Bulletins

Search:

Search History

DATE: Tuesday, December 09, 2003 [Printable Copy](#) [Create Case](#)

Set Name Query
side by side*DB=USPT; PLUR=YES; OP=ADJ*

		<u>Hit Count</u>	<u>Set Name</u>
			result set
<u>L12</u>	L3 and horizontal scan same blanking adj period same write adj data	2	<u>L12</u>
<u>L11</u>	L3 and horizontal scan same blanking adj period same write	25	<u>L11</u>
<u>L10</u>	L3 and horizontal scan same blanking adj period	110	<u>L10</u>
<u>L9</u>	L7 and write adj data same blanking adj period	7	<u>L9</u>
<u>L8</u>	L7 and write adj data and blanking adj period	24	<u>L8</u>
<u>L7</u>	L6 and blanking	24	<u>L7</u>
<u>L6</u>	L3 and write adj data same horizontal adj scan\$4	24	<u>L6</u>
<u>L5</u>	L3 and write adj data same blanking adj period	28	<u>L5</u>
<u>L4</u>	L3 and write adj data	260	<u>L4</u>
<u>L3</u>	L2 and blanking adj period	2537	<u>L3</u>
<u>L2</u>	L1 and blanking	11346	<u>L2</u>
<u>L1</u>	display	420452	<u>L1</u>

END OF SEARCH HISTORY

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Term	Documents
DRIVE	725256
DRIVES	281506
DATA	805062
DATUM	14462
IMAGE	423136
IMAGES	209925
PREDETERMINED	1055480
PREDETERMINEDS	0
DELAY	275438
DELAYS	88553
CIRCUIT\$1	0
((DRIVE OR DATA) ADJ CIRCUIT\$1 SAME IMAGE SAME PREDETERMINED ADJ DELAY).USPT.	4

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Database:**Search:**

L33

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Search History

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<u>Set Name</u>	<u>Query</u>	<u>Hit Count</u>	<u>Set Name</u>
		result set	
<u>L33</u>	(drive or data) adj circuit\$1 same image same predetermined adj delay	4	<u>L33</u>
<u>L32</u>	(drive or data) adj circuit\$1 same image and predetermined adj delay	51	<u>L32</u>
<u>L31</u>	(drive or data) adj circuit\$1 and image and predetermined adj delay	140	<u>L31</u>
<u>L30</u>	(drive or data) adj circuit\$1 and image and predetermimed adj delay	0	<u>L30</u>
<u>L29</u>	drive adj circuit\$1 and image and predetermimed adj delay	0	<u>L29</u>
<u>L28</u>	drive adj circuit\$1 same image and predetermimed adj delay	0	<u>L28</u>
<u>L27</u>	drive adj circuit\$1 same image adj delay	0	<u>L27</u>
<u>L26</u>	L23 and drive adj circiut\$1	0	<u>L26</u>
<u>L25</u>	L23 and drive adn circiut\$1	0	<u>L25</u>
<u>L24</u>	L23 and write adj image adj data	1	<u>L24</u>
<u>L23</u>	I21 and image adj data same predetermined adj delay	41	<u>L23</u>
<u>L22</u>	L21 and predetermined adj delay	339	<u>L22</u>
<u>L21</u>	image adj data	54620	<u>L21</u>
<u>L20</u>	L1 and data adj signal adj line\$1 same writ\$4 adj (image or data) same predetermined adj delay\$3	0	<u>L20</u>
<u>L19</u>	L1 and data adj signal adj line\$1 same writ\$4 adj (image or data) same delay\$3	9	<u>L19</u>
<u>L18</u>	L1 and data adj signal adj line\$1 same writ\$4 adj (image or data) and delay\$3	43	<u>L18</u>
<u>L17</u>	L16 and delay\$3	43	<u>L17</u>
<u>L16</u>	L1 and data adj signal adj line\$1 same writ\$4 adj (image or data)	88	<u>L16</u>
<u>L15</u>	L1 and data adj signal adj line\$1 same writes adj image adj data	0	<u>L15</u>
<u>L14</u>	L1 and data adj signal adj line\$1 same writes adj image data	0	<u>L14</u>
<u>L13</u>	L1 and data adj signal adj line\$1	1510	<u>L13</u>
<u>L12</u>	L3 and horizontal scan same blanking adj period same write adj data	2	<u>L12</u>
<u>L11</u>	L3 and horizontal scan same blanking adj period same write	25	<u>L11</u>
<u>L10</u>	L3 and horizontal scan same blanking adj period	110	<u>L10</u>
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<u>L7</u>	L6 and blanking	24	<u>L7</u>
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<u>L4</u>	L3 and write adj data	260	<u>L4</u>
<u>L3</u>	L2 and blanking adj period	2537	<u>L3</u>
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<u>L1</u>	display	420452	<u>L1</u>

END OF SEARCH HISTORY